



Docket No.: 57454-973

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Hideto HIDAKA

Serial No.: 10/680,397

Filed: October 08, 2003

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE OPERATING WITH LOW
POWER CONSUMPTION

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Confirmation No: 5812
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Group Art Unit: 2812
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Examiner: Unknown
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PRELIMINARY AMENDMENT

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Prior to examination of the above-referenced application, please amend the application as
follows:

IN THE SPECIFICATION: